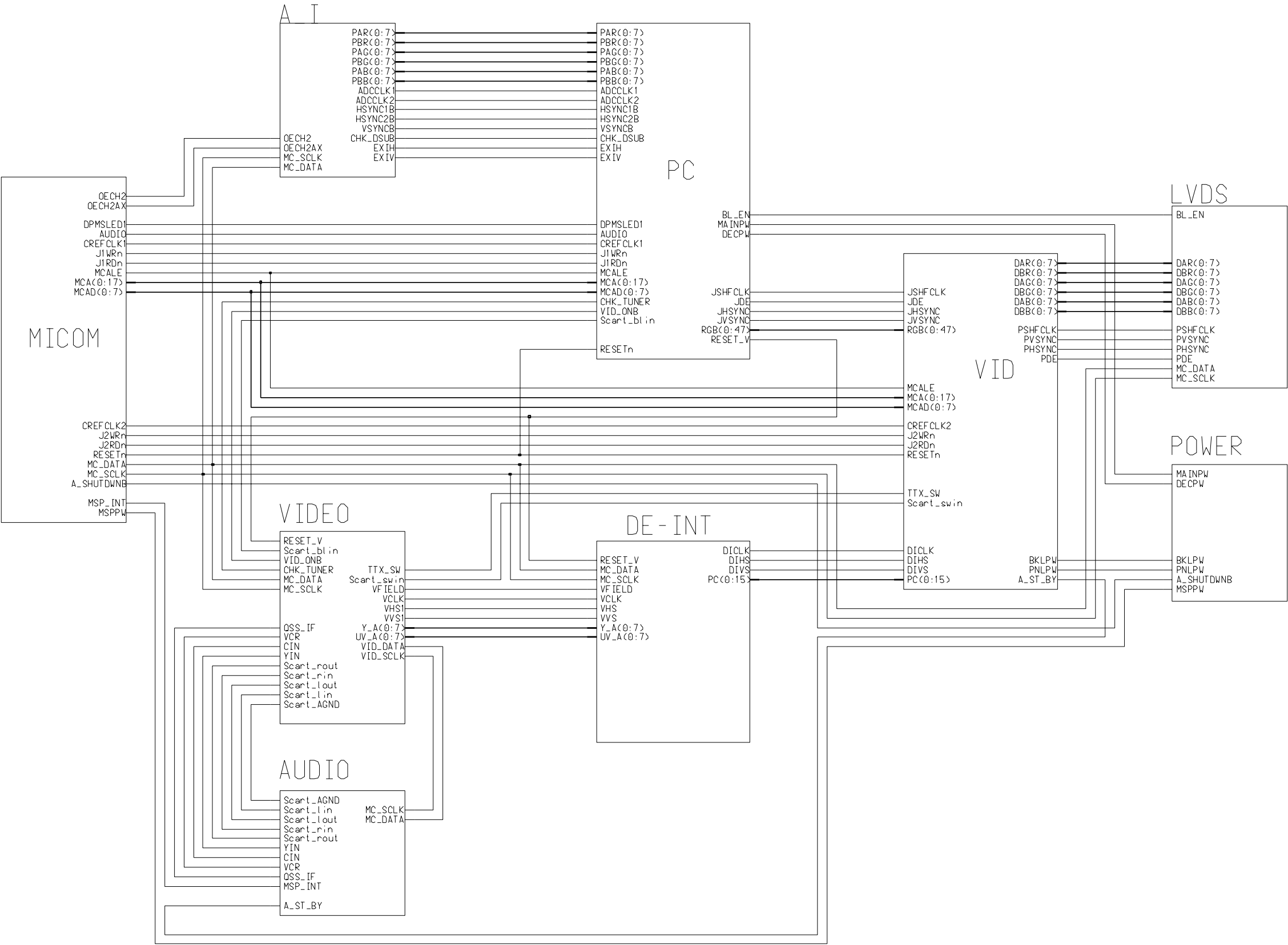


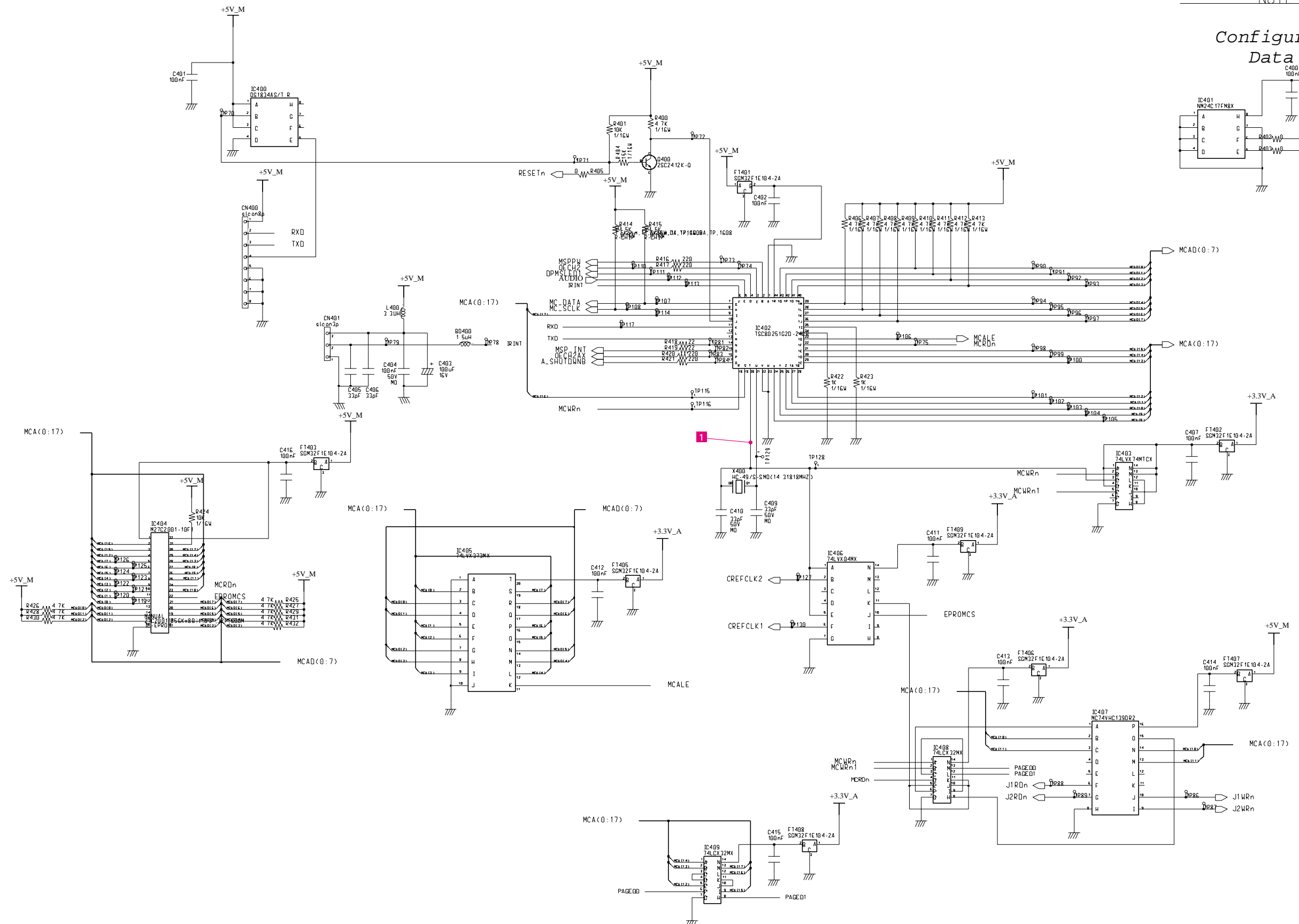
10 Schematic Diagrams

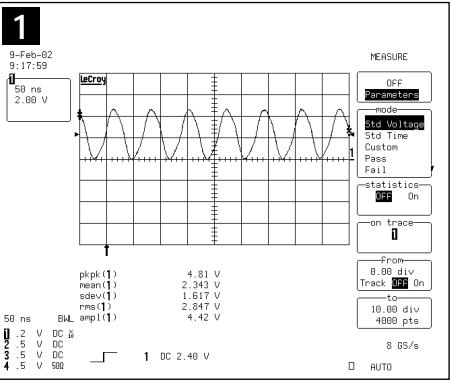
10-1 RUBENS_PAL_Top Parts Schematic Diagram

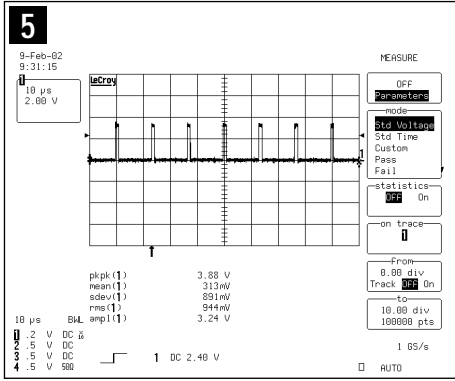
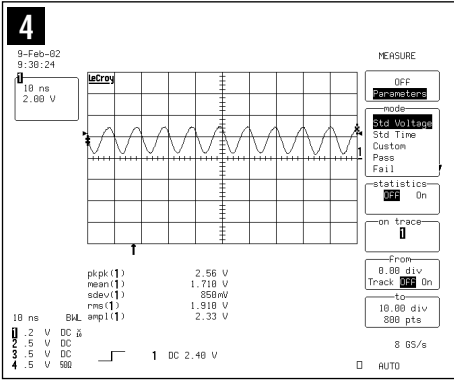
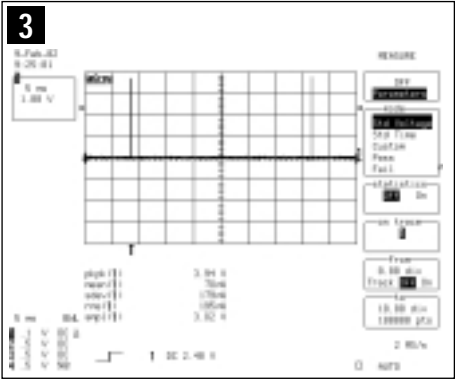
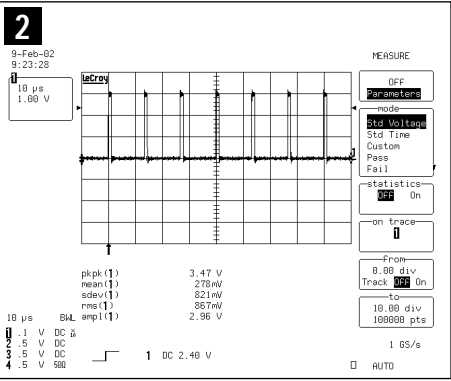


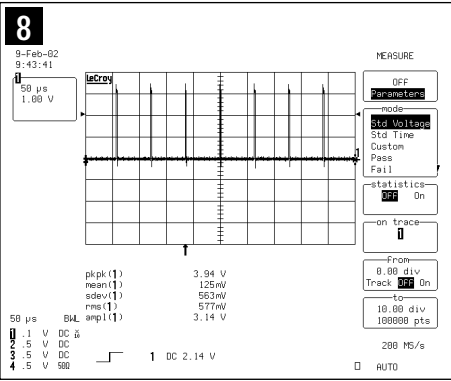
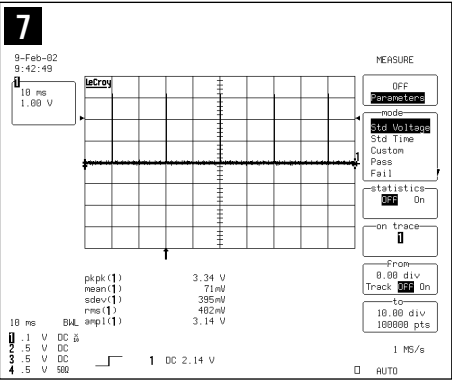
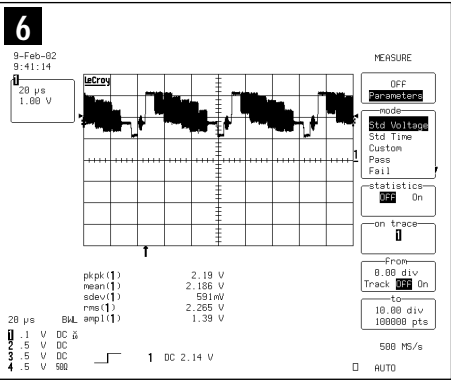
NOTE

The circuit diagram shows an IC401 NM24C17FN8X chip. The VCC pin (pin 1) is connected to a +3.3V supply through a 1kΩ resistor. The GND pin (pin 2) is connected to ground. The I/O pin (pin 3) is connected to the Data bus through a 1kΩ resistor. The chip has four data pins (pins 4, 5, 6, 7) labeled A, B, C, and D. These pins are connected to the Data bus through 1kΩ resistors. The chip also has two status pins (pins 8, 9) labeled MC_SCLK and MC_DATA, which are connected to the Data bus through 1kΩ resistors. The chip is labeled "IC401 NM24C17FN8X".

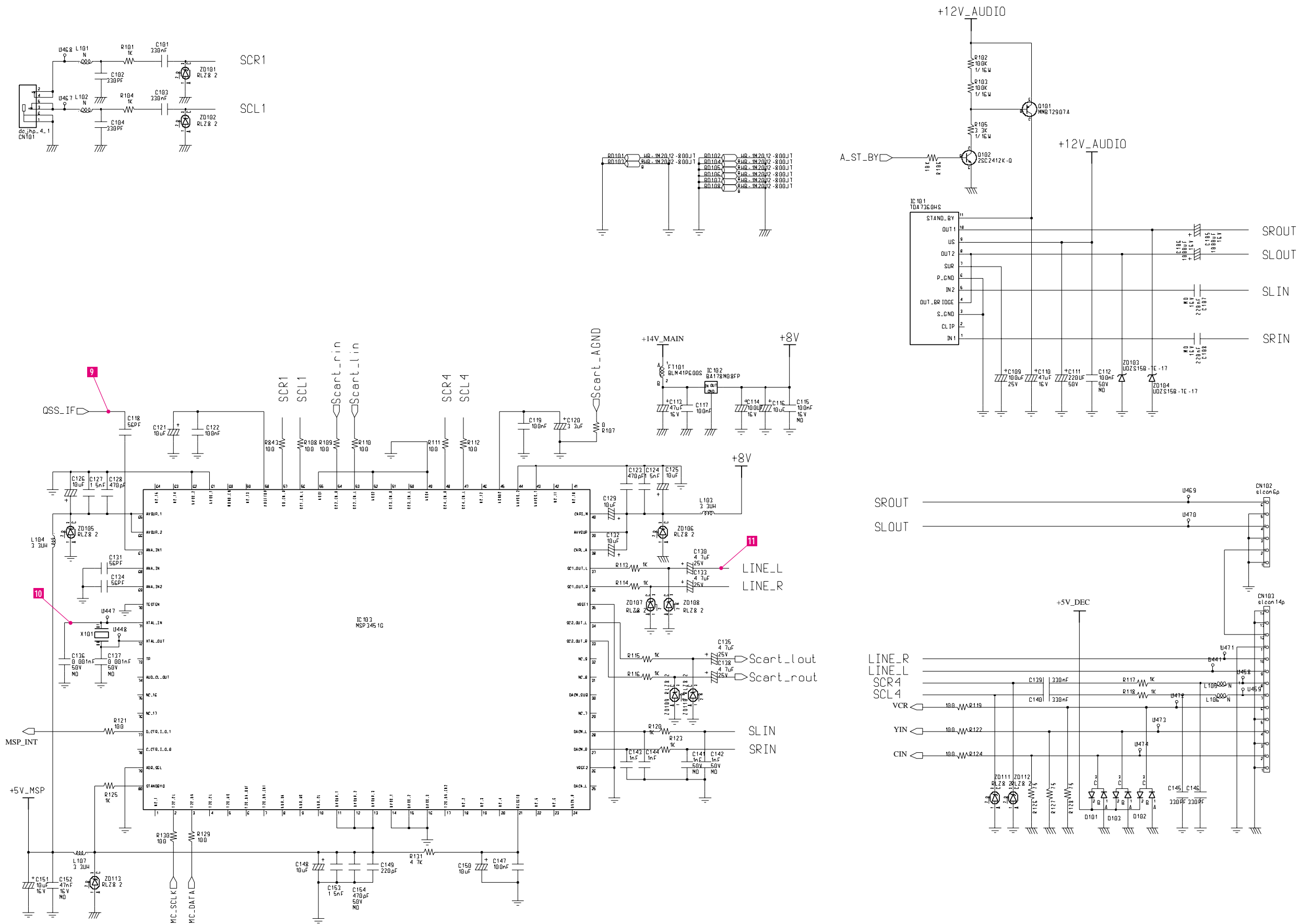


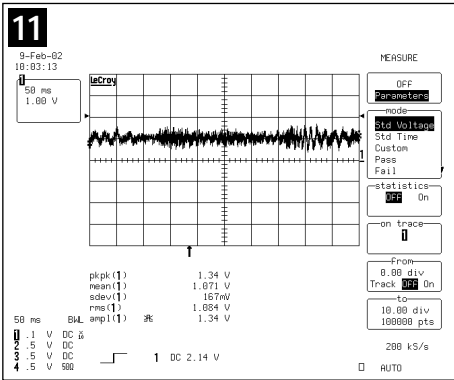
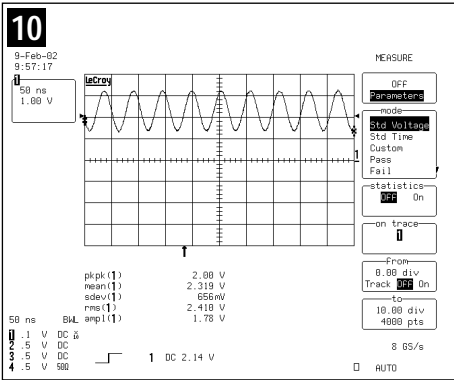
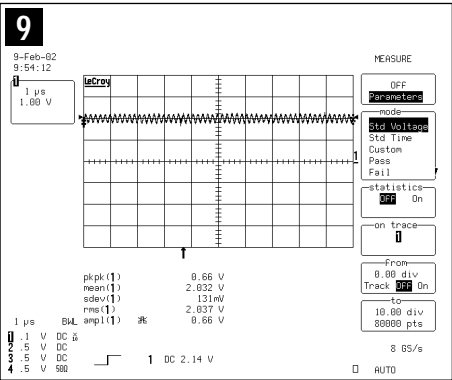






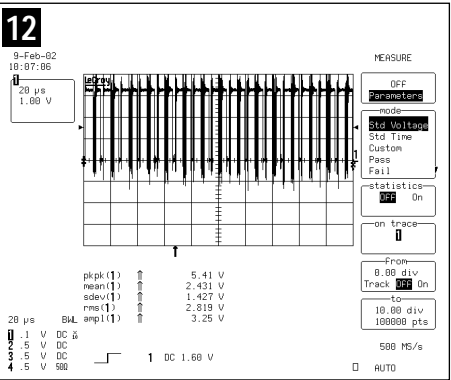
10-5 RUBENS_PAL_Audio Parts Schematic Diagram



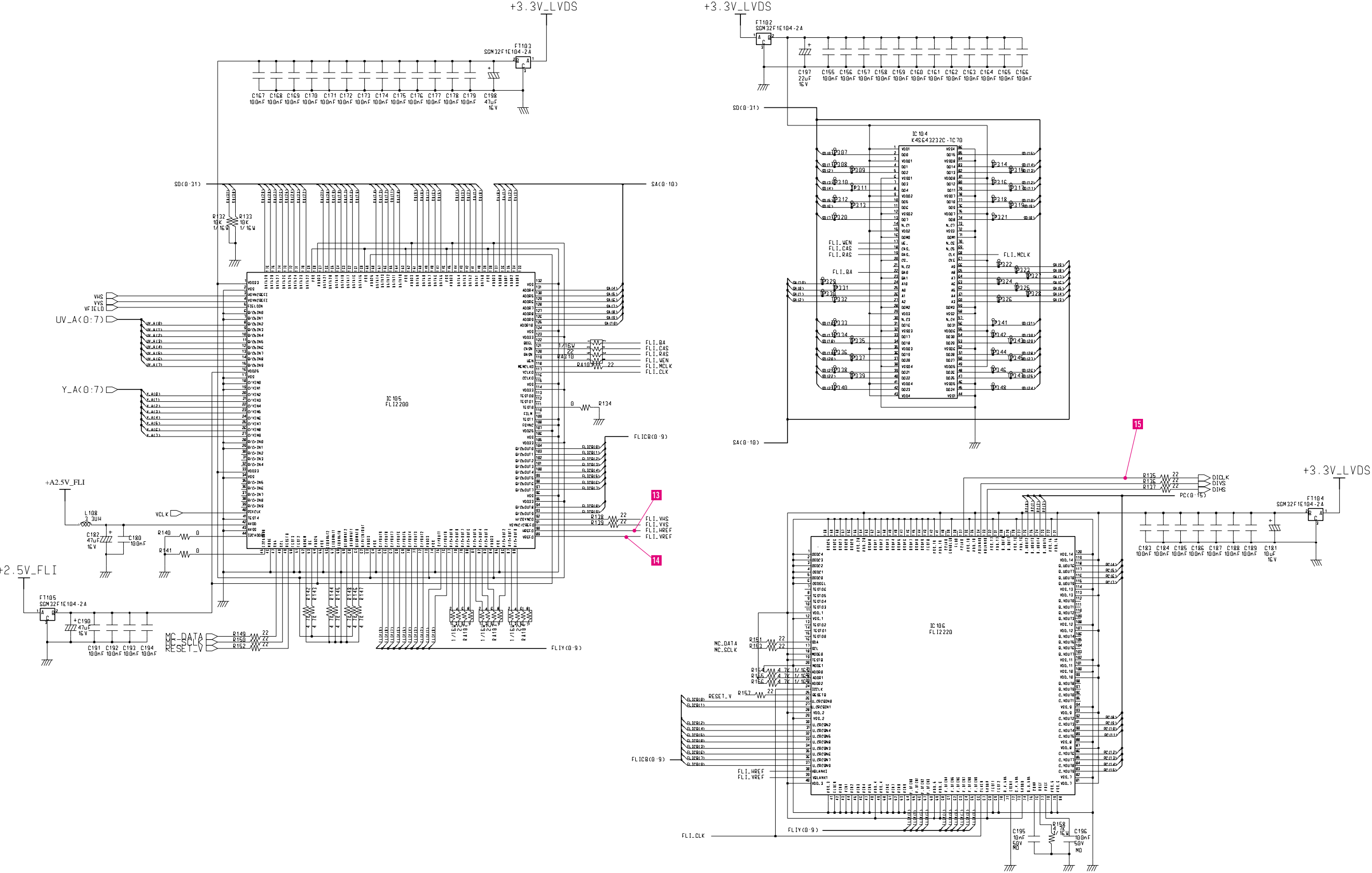


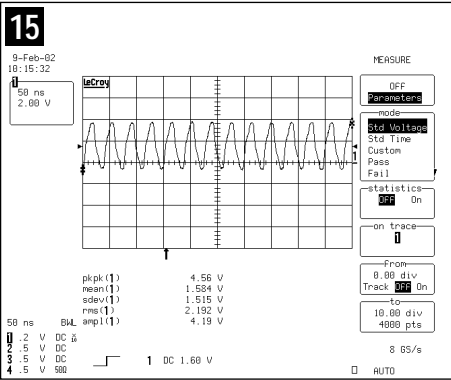
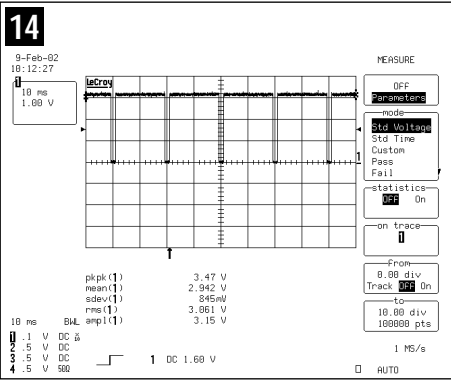
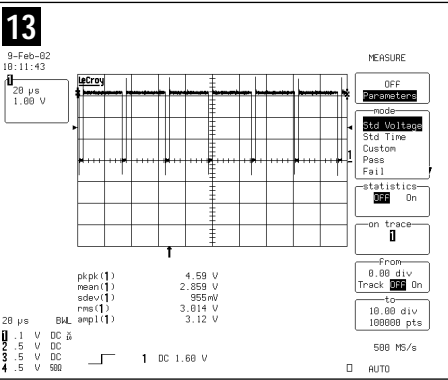
10-10



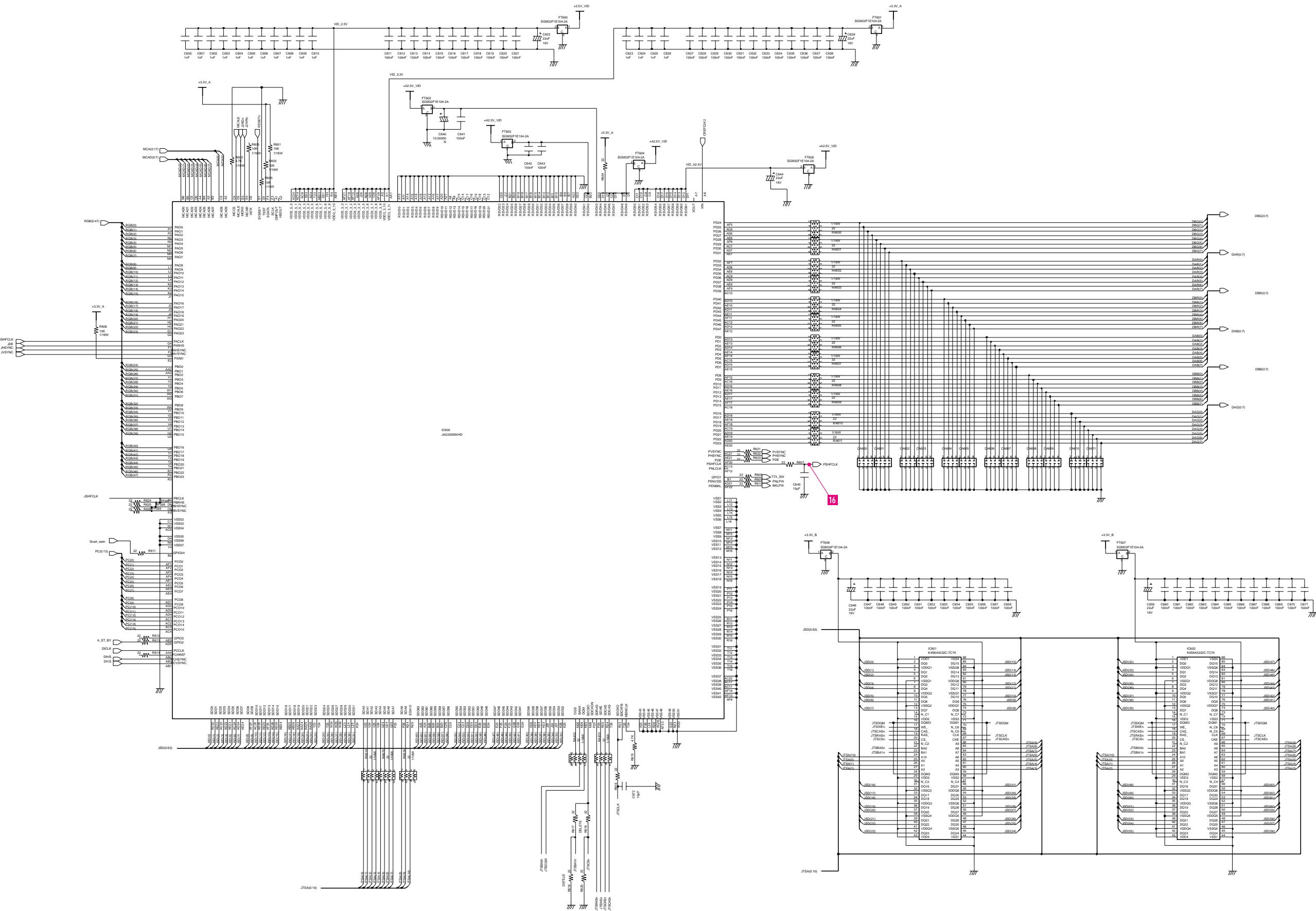


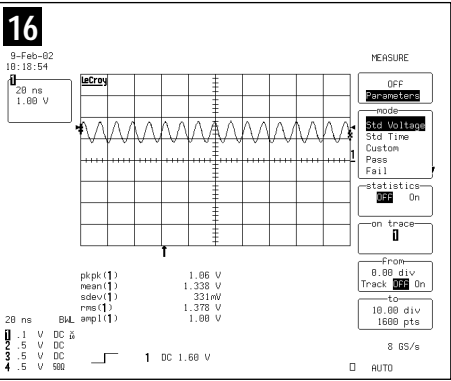
10-7 RUBENS_PAL_V_Backend Parts Schematic Diagram



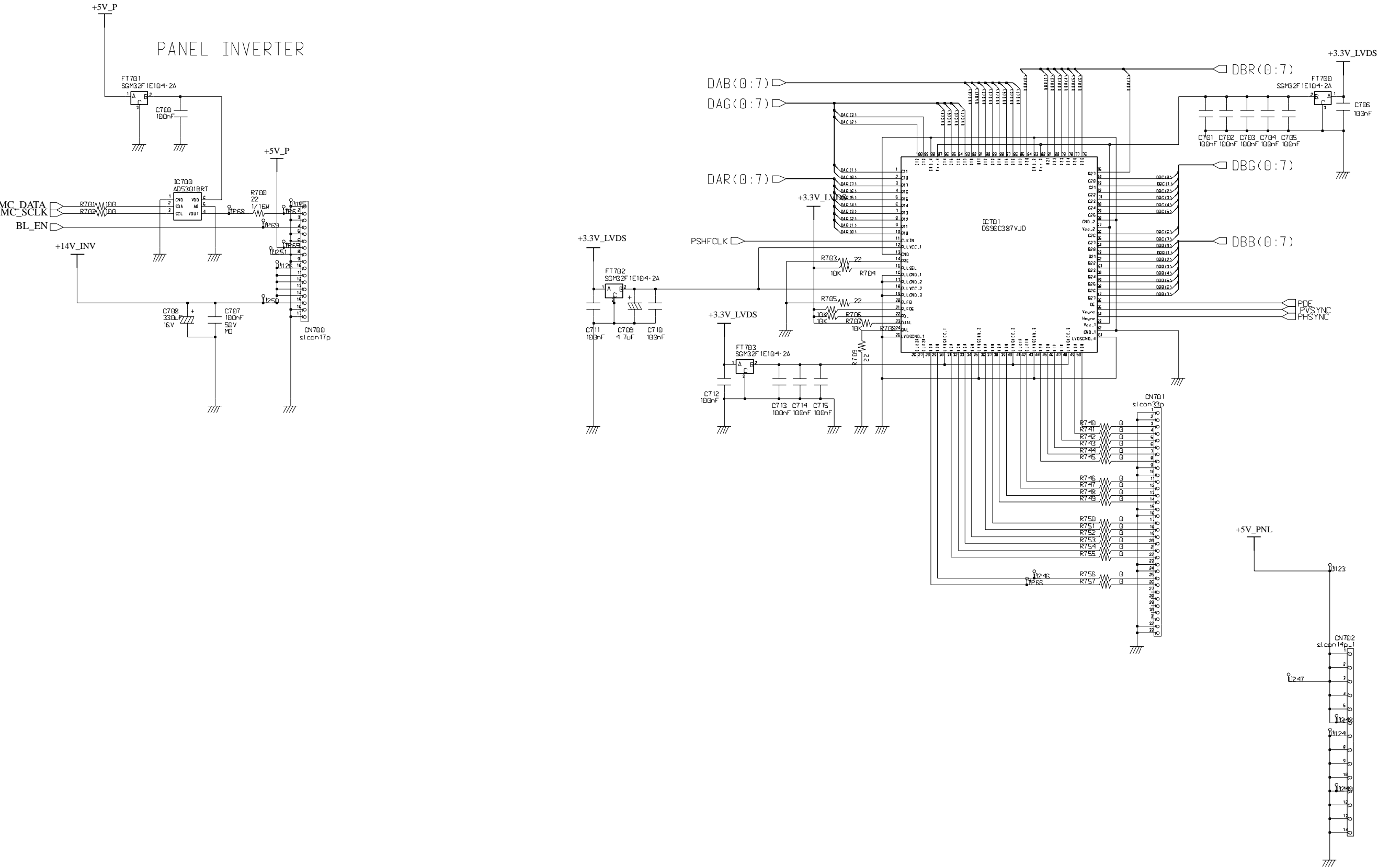


10-8 RUBENS_PAL_J200_VID Parts Schematic Diagram





10-9 RUBENS_PAL_LVDS Parts Schematic Diagram



10-10 RUBENS_PAL_POWER Parts Schematic Diagram

